# SDCC - Small Device C Compiler

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#### What is SDCC?

SDCC is a retargettable, optimizing ANSI - C compiler suite that targets the Intel MCSSI based microprocessors (8031, 8032, 8051, 8052, etc.), Maxim (formerly Dullas) DSSDC390 variants, Pre-escale (formerly Motoroids) HC0S based (Moto, 808) and Zilog ZSO based MCUs (280, 2180, gbz80, Rabbit 2000/3000, Rabbit 3000A). Work is in progress on supporting the Microchip PIC16 and PSC18 targets. It can be retargeted for other microprocessors.

SDCC suite is a collection of several components derived from different sources with different FOSS licenses. SDCC compiler suite include:

- sdas and sdld, a retargettable assembler and linker, based on ASXXXX, written by Alan Baldwin; (GPL).
- sdopp preprocessor, based on GCC cap: (GPL).
- ucsim simulators, originally written by Daniel Drotos; (GPL).
  sdodb source level debugger, originally written by Sandeep Dutta; (GPL).
- adbinutils library archive utilities, including sdar, sdranib and sdnm, derived from GNU Binutils; (GPL)
- SDCC run-time Bbraries; (GPL+LE). Pic device libraries and header files are derived from Microchip header (.inc) and limiter script (.ibr) files. Microchip requires that "The header files should state that they are only to be used with authentic Microchip devices" which makes them incompatible with the GPL.
- gcc-test regression tests, derived from gcc-testswite; (no license explicitely specified, but since it is a part of GCC is probably GPL licensed)
- packihx; (public domain)
- makebin; (zilb/libpng License)
- sdoc C compiler, originally written by Sandoep Dutta; (GPL). Some of the features include:
  - extensive MCU specific language extensions, allowing effective use of the underlying hardware.
  - a host of standard optimisations such as global sub expression elimination, loop optimisations (loop invariant, strength reduction of induction variables and loop reversing), constant folding and propagation, copy propagation, dead code elimination and jump tables for 'switch' statements.
  - MCU specific optimizations, including a global register allocator.
  - adaptable MCU specific backend that should be well suited for other 8 bit MCUs
  - · independent rule based peep hole optimizer.
  - a full range of data types: char (8 bits, 1 byte), short (16 bits, 2 bytes), lint (16 bits, 2 bytes), long (32 bit, 4 bytes), float (4 byte IEEE) and \_Bool/bool;
    basic (no integer constants) support for long long (64 bit, 8 bytes) data types for the z80, z180, r2k, r3ka, gbz80, hc08 and s08 targets.
  - the ability to add inline assembler code anywhere in a function.
  - the ability to report on the complexity of a function to help decide what should be re-written in assembler.
  - . a good selection of automated regression tests.

**SDCC** was written by Sandeep Dutta and released under a **GPL** license. Since its initial release there have been numerous bug flixes and improvements. As of December 1999, the code was moved to SourceForge where all the "users turned developers" can access the same source tree. SDCC is constantly being updated with all the users' and developers' input.

#### News

#### May 20th, 2013: Small Device C Compiler 3.3.0 released.

A new release of SDCC, the portable optimizing compiler for 8051, DS390, Z80, Z180, Rabbit 2000, HC08 and PIC microprocessors is now available (http://sdcc.uouereforge.net). Sources, documentation and binaries compiled for x86 Unux, x86 and x64 MS Windows and x86 and PPC Mac OS X are available.

SDCC 3.3.0 Feature List:

- . Many small improvements in code generation for the z80-related ports merged smallopts branch
- lospre (currently enabled for z80-related and hc08-related ports only) merged lospre branch
- More efficient initialization of globals in x80, x180, r2k and r3ka ports.
- Inclusion of tests from the gcc test suite into the sdcc regression test suite led to many bugs being found and fixed.
- Split sdas390 from sdas8051.
- Merged big parts of ASxxxx v5 into sdas
- . New pic devices (synchronization with MPLABX 1.60). (Except for very old MCU-s.)
- New script which disassembles those hex files, in which MCSS1 code there is: (mcsS1-disasm.pl)
- Added the PIC16F1788 and PIC16F1789 devices.
- C11 \_Alignof operator.
- C11 \_Alignas alignment specifier.
- C11 \_Static\_Assert static assertion.

Numerous feature requests and bug fixes are included as well.

You can download the release from: https://sourceforce.net/projects/sdcc/files/

#### What Platforms are Supported?

Linux - x86, Hicrosoft Windows - x86, Hicrosoft Windows - x86\_64 and Hac OS X are the primary, so called "officially supported" platforms.

# **Small Device C Compiler Manual**

**Lingjun Ying** 

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