

Power Efficient System Design

- Bandwidth expansion (e.g. from modulation and/or coding) should be used efficiently to improve link performance
 - Reduces SWaP and cost of terminals
 - Allows future scaling to higher data rates
- Space-relay and/or Direct-to-Earth architectures enable use of capacity-approaching coding techniques, since high-complexity decoding may be performed at ground terminal

Power Efficient System Design

Eng Hwa Yap



Power Efficient System Design:

Power-efficient System Design Preeti Ranjan Panda, B. V. N. Silpa, Aviral Shrivastava, Krishnaiah Gummidipudi, 2010-07-23 The Information and communication technology ICT industry is said to account for 2% of the worldwide carbon emissions a fraction that continues to grow with the relentless push for more and more sophisticated computing equipment communications infrastructure and mobile devices While computers evolved in the direction of higher and higher performance for most of the latter half of the 20th century the late 1990s and early 2000s saw a new emerging fundamental concern that has begun to shape our day to day thinking in system design power dissipation As we elaborate in Chapter 1 a variety of factors colluded to raise power efficiency as a first class design concern in the designer's mind with profound consequences all over the old semiconductor process design circuit design design automation tools system and application software all the way to large data centers Power efficient System Design originated from a desire to capture and highlight the exciting developments in the rapidly evolving field of power and energy optimization in electronic and computer based systems Tremendous progress has been made in the last two decades and the topic continues to be a fascinating research area To develop a clearer focus we have concentrated on the relatively higher level of design abstraction that is loosely called the system level In addition to the extensive coverage of traditional power reduction targets such as CPU and memory the book is distinguished by detailed coverage of relatively modern power optimization ideas focussing on components such as compilers operating systems servers data centers and graphics processors

System-Level Design Techniques for Energy-Efficient Embedded Systems Marcus T. Schmitz, Bashir M. Al-Hashimi, Petru Eles, 2006-01-16 System Level Design Techniques for Energy Efficient Embedded Systems addresses the development and validation of co synthesis techniques that allow an effective design of embedded systems with low energy dissipation The book provides an overview of a system level co design flow illustrating through examples how system performance is influenced at various steps of the flow including allocation mapping and scheduling The book places special emphasis upon system level co synthesis techniques for architectures that contain voltage scalable processors which can dynamically trade off between computational performance and power consumption Throughout the book the introduced co synthesis techniques which target both single mode systems and emerging multi mode applications are applied to numerous benchmarks and real life examples including a realistic smart phone

Custom Memory Management Methodology Francky Catthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 2013-03-09 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989-1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and

optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large amounts of data **Energy**

Efficient Buildings Eng Hwa Yap,2017-01-18 This book discusses energy efficient buildings and the role they play in our efforts to address climate change energy consumption and greenhouse gas emissions by considering buildings and the construction sector s unique position along a critical path to decarbonisation from a multi perspective and holistic viewpoint Topics covered in the book range from daylighting building topology comparison building envelope design zero energy homes in hot arid regions life cycle considerations and energy efficiency analysis to managing energy demand through equipment selection Each chapter addresses an important aspect of energy efficient building and serves as a vital building block towards constructing a timely and relevant body of knowledge in energy efficient buildings *Handbook of Research on*

Advanced Hybrid Intelligent Techniques and Applications Bhattacharyya, Siddhartha,Banerjee, Pinaki,Majumdar, Dipankar,Dutta, Paramartha,2015-11-03 Conventional computational methods and even the latest soft computing paradigms often fall short in their ability to offer solutions to many real world problems due to uncertainty imprecision and circumstantial data Hybrid intelligent computing is a paradigm that addresses these issues to a considerable extent The Handbook of Research on Advanced Hybrid Intelligent Techniques and Applications highlights the latest research on various issues relating to the hybridization of artificial intelligence practical applications and best methods for implementation Focusing on key interdisciplinary computational intelligence research dealing with soft computing techniques pattern mining data analysis and computer vision this book is relevant to the research needs of academics IT specialists and graduate level students **System Specification and Design Languages** Tom J. Kaźmierski,Adam Morawiec,2011-12-02 This book

brings together a selection of the best papers from the thirteenth edition of the Forum on specification and Design Languages Conference FDL which was held in Southampton UK in September 2010 FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modelling and verification of integrated circuits complex hardware software embedded systems and mixed technology systems **Energy Management in Wireless Cellular and Ad-hoc Networks**

Muhammad Zeeshan Shakir,Muhammad Ali Imran,Khalid A. Qaraqe,Mohamed-Slim Alouini,Athanasios V. Vasilakos,2016-01-14 This book investigates energy management approaches for energy efficient or energy centric system

design and architecture and presents end to end energy management in the recent heterogeneous type wireless network medium It also considers energy management in wireless sensor and mesh networks by exploiting energy efficient transmission techniques and protocols and explores energy management in emerging applications services and engineering to be facilitated with 5G networks such as WBANs VANETS and Cognitive networks A special focus of the book is on the examination of the energy management practices in emerging wireless cellular and ad hoc networks Considering the broad scope of energy management in wireless cellular and ad hoc networks this book is organized into six sections covering range of Energy efficient systems and architectures Energy efficient transmission and techniques Energy efficient applications and services

Advances in Computers Marvin Zelkowitz, Suyel Namasudra, 2005-05-19 The term computation gap has been defined as the difference between the computational power demanded by the application domain and the computational power of the underlying computer platform Traditionally closing the computation gap has been one of the major and fundamental tasks of computer architects However as technology advances and computers become more pervasive in the society the domain of computer architecture has been extended The scope of research in the computer architecture is no longer restricted to the computer hardware and organization issues A wide spectrum of topics ranging from algorithm design to power management is becoming part of the computer architecture Based on the aforementioned trend and to reflect recent research efforts attempts were made to select a collection of articles that covers different aspects of contemporary computer architecture design This volume of the *Advances in Computers* contains six chapters on different aspects of computer architecture Key features Wide range of research topics Coverage of new topics such as power management Network on Chip Load balancing in distributed systems and pervasive computing Simple writing style Wide range of research topics Coverage of new topics such as power management Network on Chip Load balancing in distributed systems and pervasive computing Simple writing style

Encyclopedia of Parallel Computing David Padua, 2011-09-08 Containing over 300 entries in an A Z format the *Encyclopedia of Parallel Computing* provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The *Encyclopedia* is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the *Encyclopedia* support efficient user friendly searchers for immediate access to useful information Key concepts presented in the *Encyclopedia of Parallel Computing* include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors

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Advances in Computers Marvin Zelkowitz,2005 The term computation gap has been defined as the difference between the computational power demanded by the application domain and the computational power of the underlying computer platform Traditionally closing the computation gap has been one of the major and fundamental tasks of computer architects However as technology advances and computers become more pervasive in the society the domain of computer architecture has been extended The scope of research in the computer architecture is no longer restricted to the computer hardware and organization issues A wide spectrum of topics ranging from algorithm design to power management is becoming part of the computer architecture Based on the aforementioned trend and to reflect recent research efforts attempts were made to select a collection of articles that covers different aspects of contemporary computer architecture design This volume of the Advances in Computers contains six chapters on different aspects of computer architecture Key features Wide range of research topics Coverage of new topics such as power management Network on Chip Load balancing in distributed systems and pervasive computing Simple writing style Wide range of research topics Coverage of new topics such as power management Network on Chip Load balancing in distributed systems and pervasive computing Simple writing style

Power Aware Design Methodologies Massoud Pedram,Jan M. Rabaey,2007-05-08 Power Aware Design Methodologies was conceived as an effort to bring all aspects of power aware design methodologies together in a single document It covers several layers of the design hierarchy from technology circuit logic and architectural levels up to the system layer It includes discussion of techniques and methodologies for improving the power efficiency of CMOS circuits digital and analog systems on chip microelectronic systems wirelessly networked systems of computational nodes and so on In addition to providing an in depth analysis of the sources of power dissipation in VLSI circuits and systems and the technology and design trends this book provides a myriad of state of the art approaches to power optimization and control The different chapters of Power Aware Design Methodologies have been written by leading researchers and experts in their respective areas Contributions are from both academia and industry The contributors have reported the various technologies methodologies and techniques in such a way that they are understandable and useful

Ultra Low-Power Electronics and Design E. Macii,2007-05-08 Power consumption is a key limitation in many high speed and high data rate electronic systems today ranging from mobile telecom

to portable and desktop computing systems especially when moving to nanometer technologies Ultra Low Power Electronics and Design offers to the reader the unique opportunity of accessing in an easy and integrated fashion a mix of tutorial material and advanced research results contributed by leading scientists from academia and industry covering the most hot and up to date issues in the field of the design of ultra low power devices systems and applications

Math for the Digital Factory Luca Ghezzi, Dietmar Hömberg, Chantal Landry, 2017-10-30 This volume provides a unique collection of mathematical tools and industrial case studies in digital manufacturing It addresses various topics ranging from models of single production technologies production lines logistics and workflows to models and optimization strategies for energy consumption in production The digital factory represents a network of digital models and simulation and 3D visualization methods for the holistic planning realization control and ongoing improvement of all factory processes related to a specific product In the past ten years all industrialized countries have launched initiatives to realize this vision sometimes also referred to as Industry 4.0 in Europe or Smart Manufacturing in the United States Its main goals are reconfigurable adaptive and evolving factories capable of small scale production high performance production combining flexibility productivity precision and zero defects energy and resource efficiency in manufacturing None of these goals can be achieved without a thorough modeling of all aspects of manufacturing together with a multi scale simulation and optimization of process chains in other words without mathematics To foster collaboration between mathematics and industry in this area the European Consortium for Mathematics in Industry ECMI founded a special interest group on Math for the Digital Factory M4DiFa This book compiles a selection of review papers from the M4DiFa kick off meeting held at the Weierstrass Institute for Applied Analysis and Stochastics in Berlin Germany in May 2014 The workshop aimed at bringing together mathematicians working on modeling simulation and optimization with researchers and practitioners from the manufacturing industry to develop a holistic mathematical view on digital manufacturing This book is of interest to practitioners from industry who want to learn about important mathematical concepts as well as to scientists who want to find out about an exciting new area of application that is of vital importance for today's highly industrialized and high wage countries

Energy-Aware Memory Management for Embedded Multimedia Systems Florin Balasa, Dhiraj K. Pradhan, 2011-11-16 Energy Aware Memory Management for Embedded Multimedia Systems A Computer Aided Design Approach presents recent computer aided design CAD ideas that address memory management tasks particularly the optimization of energy consumption in the memory subsystem It explains how to efficiently implement CAD solutions including theoretical methods and

11th US/North American Mine Ventilation Symposium 2006 Jan M. Mutmansky, Raja. V. Ramani, 2006-05-18 This volume is the eleventh in a series which documents the technical papers of the mine ventilation symposium which was initiated in 1982 by the Underground Ventilation Committee of the Society for Mining Metallurgy and Exploration Inc In more recent years the event has expanded to include all of North America and is known as the US North Am

Futuristic Trends in Network and Communication

Technologies Pradeep Kumar Singh, Gennady Veselov, Anton Pljonkin, Yugal Kumar, Marcin Paprzycki, Yuri Zachinyaev, 2021-03-30 This two volume set CCIS 1395 1396 constitutes the refereed proceedings of the Third International Conference on Futuristic Trends in Network and Communication Technologies FTNCT 2020 held in Taganrog Russia in October 2020 The 80 revised papers presented were carefully reviewed and selected from 291 submissions The prime aim of the conference is to invite researchers from different domains of network and communication technologies to a single platform to showcase their research ideas The selected papers are organized in topical sections on communication technologies security and privacy futuristic computing technologies network and computing technologies wireless networks and Internet of Things IoT *Low-Power Variation-Tolerant Design in Nanometer Silicon* Swarup Bhunia, Saibal Mukhopadhyay, 2010-11-10 Design considerations for low power operations and robustness with respect to variations typically impose contradictory requirements Low power design techniques such as voltage scaling dual threshold assignment and gate sizing can have large negative impact on parametric yield under process variations This book focuses on circuit architectural design techniques for achieving low power operation under parameter variations We consider both logic and memory design aspects and cover modeling and analysis as well as design methodology to achieve simultaneously low power and variation tolerance while minimizing design overhead This book will discuss current industrial practices and emerging challenges at future technology nodes Success by Design: Engineering Systems for Sustainable Growth Silas Mary, 2025-01-23 Success by Design reveals the secrets to creating robust systems that foster sustainable growth in business This book offers step by step guidance on how to design and implement business systems that support scalability efficiency and long term success From streamlining operations to optimizing workflows Success by Design shows you how to build a business that can thrive in both the short term and the long term Through detailed frameworks and case studies this book helps you engineer the processes tools and strategies needed to scale your business effectively Success by Design is perfect for entrepreneurs and business leaders who want to build a solid foundation that enables continued growth and success Intelligent Communication, Control and Devices Adesh Kumar, Rupendra Kumar Pachauri, Ranjan Mishra, Piyush Kuchhal, 2025-02-01 This book focuses on the integration of intelligent communication systems control systems and devices related to all aspects of engineering and sciences It includes high quality research papers from the 6th International Conference on Intelligent Communication Control and Devices ICICCD 2024 organized by the Department of Electrical Electronics Engineering School of Advanced Engineering at UPES Dehradun India during May 30 31 2024 The topics covered are a range of recent advances in intelligent communication intelligent control intelligent devices and sustainable technologies **Energy Management in Illuminating Systems** Kao Chen, 2018-02-06 As our dependence on and need for abundant energy grows it becomes increasingly important for engineers and managers to develop and maintain energy efficient systems and build effective energy management programs Energy Management in Illuminating Systems presents

the latest concepts innovative methods and state of the art technologies in commercial or industrial lighting systems and energy management An effective energy management program comprises three essential elements organization technology and economics However the success of any management program clearly must begin with an energy effective illuminating system which in turn depends upon using sound engineering analysis and design principles during the projects early stages In this book the author with long and unique experience in the field provides the details of proven methods for achieving these goals He presents How to organize and operate the illumination energy management program The elements of designing energy effective illuminating systems systems that can also increase worker productivity and reduce operating costs The latest in efficient system components including light sources ballasts and luminaires How to evaluate energy efficiency including discussion of the impact of energy efficient equipment on power quality harmonics the K factor and lighting energy standards Energy Management in Illuminating Systems shows how to design and manage energy effective lighting systems for industrial or commercial facilities With this book designers engineers and managers finally have a complete how to guide for applying practical energy management principles to various systems of illumination

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