USN

15ELN15/25

#### First/Second Semester B.E. Degree(CBCS)Examination

#### **Basic Electronics**

Time: 3 hrs.

Max. Marks: 80

(05 Marks)

Note: Answer any FIVE full questions, choosing one full question from each module.

#### Module-1

- a. Define following diode parameters:
  - i) PIV ii) Knee voltage iii) Dynamic resistance. (06 Marks)
  - With neat circuit diagram and waveforms, explain the working of a half wave rectifier.
     (06 Marks)
  - Draw common emitter circuit and sketch the input and output characteristics. Also explain
    operating regions by indicating them on characteristics curve. (04 Marks)

#### $\mathbf{OR}$

- 2 a. Explain zener diode voltage regulator circuit with no load and with load. (06 Marks)
  - b. The i/p to the Full wave rectifier is v(t) = 200 sin50t. If R<sub>L</sub> is 1kΩ and forward resistance of diode is 50Ω, find:
    - i) D.C current through the circuit
    - ii) The A.C (rms) value of current through the circuit
    - iii) The D.C output voltage
    - iv) The A.C power input
    - v) The D.C power output
    - vi) Rectifier efficiency. (06 Marks)

      Derive the relationship between a and 8. Calculate the value of L. L. for a transistor that has
    - c. Derive the relationship between α and β. Calculate the value of I<sub>c</sub>, I<sub>e</sub> for a transistor that has α = 0.98 and I<sub>b</sub> = 100 μA. (04 Marks)

#### Module-2

- 3 a. What is a Dc load line? Explain Base biased method with necessary equations. (05 Marks)
  - Explain the characteristics of an Ideal Op-amp. Mention two applications of Op-Amp.
     (06 Marks)
  - With a neat circuit diagram, explain the Voltage Divider Bias circuit using approximate analysis.

#### OR.

- 4 a. Design the voltage divider bias circuit to operate from 12 V supply. The bias conditions are V<sub>CE</sub>=3V, V<sub>E</sub>=5V, and I<sub>c</sub> = 1mA. (05 Marks)
  - b. Derive the expression of 3 input summing amplifier.
  - Explain voltage follower with neat circuit and necessary equations. (06 Marks)

## Module-3

- 5 a. Convert  $(1101101)_2 = ($   $)_{10}$  and  $(69)_{10} = ($   $)_2$  (04 Marks)
  - b. Convert (10101111011110101)<sub>2</sub> = ( )<sub>16</sub> and (FA876)<sub>16</sub> = ( )<sub>2</sub> (04 Marks)
     c. State and prove Demorgan's Theorem. (08 Marks)

# **Vtu Basic Electronics Question Papers**

Norman W. Armstrong

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Basic Electronics - Second Edition B Basavaraj,2009-11-01 This is an established textbook on Basic Electronics for engineering students It has been revised according to the latest syllabus The second edition of the book includes illustrations and detailed explanations of fundamental concepts with examples The entire syllabus has been covered in 12 chapters

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