

Topdiode Manufacturing Company



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Topdiode	PANJIT	Taiwan Semiconduct or	GI	MOTORLA	IR	FUJI	FAGOR	LITE-ON
1A1	1A1	1T1						
1A2	1A2	1T2						
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1A7	1A7	1T7						
1N4001	1N4001	1N4001	1N4001	1N4001			1N4001	1N4001
1N4001S	1N4001S	1N4001S					1N4001F	1N4001L
1N4002	1N4002	1N4002	1N4002	1N4002	10D1	ERS12-01	1N4002	1N4002
1N4002S	1N4002S	1N4002S				ERS15-01	1N4002F	1N4002L
1N4003	1N4003	1N4003	1N4003	1N4003	10D2	ERS12-02	1N4003	1N4003
1N4003S	1N4003S	1N4003S				ERS15-02	1N4003F	1N4003L
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1N5404	1N5404	1N5404	1N5404	1N5404	30D4	ERD03-04	1N5404	1N5404
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# Transistor Cross Reference Guide

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## Transistor Cross Reference Guide:

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designs fabrication and modeling of various WBG semiconductor power electronics and optoelectronic devices *Popular Electronics*, 1977 *Robust Electronic Design Reference Book: no special title* John R. Barnes, 2004 If you design electronics for a living you need Robust Electronic Design Reference Book Written by a working engineer who has put over 115 electronic products into production at Sycor IBM and Lexmark Robust Electronic Design Reference covers all the various aspects of designing and developing electronic devices and systems that Work Are safe and reliable Can be manufactured tested repaired and serviced May be sold and used worldwide Can be adapted or enhanced to meet new and changing requirements *Nanoelectronic Materials, Devices and Modeling* Qiliang Li, Hao Zhu, 2019-07-15 As CMOS scaling is approaching the fundamental physical limits a wide range of new nanoelectronic materials and devices have been proposed and explored to extend and or replace the current electronic devices and circuits so as to maintain progress with respect to speed and integration density The major limitations including low carrier mobility degraded subthreshold slope and heat dissipation have become more challenging to address as the size of silicon based metal oxide semiconductor field effect transistors MOSFETs has decreased to nanometers while device integration density has increased This book aims to present technical approaches that address the need for new nanoelectronic materials and devices The focus is on new concepts and knowledge in nanoscience and nanotechnology for applications in logic memory sensors photonics and renewable energy This research on nanoelectronic materials and devices will be instructive in finding solutions to address the challenges of current electronics in switching speed power consumption and heat dissipation and will be of great interest to academic society and the industry [Smart Coatings on Fibers and Textiles](#) Mazeyar Parvinzadeh Gashti, 2020-03-25 Smart textiles are the textiles that are sensitive to any environmental conditions and can respond accordingly Using passive and active coatings to generate high sensitivity to textiles is among the most recent research trends by engineers around the World This has resulted in expansion in the application of smart textiles in various industrial fields including medicals electronics and protective clothing The aim of this special issue is to introduce the most state of the art research and review articles by distinguished researchers in the field of smart coatings on textiles The guest editor hopes that content will be useful for researchers students and companies for continuation of research and development with the concept of smart textiles

**RADIO 2** Bram Palgunadi, 2022-02-22 Buku Radio 2 Menggapai angkasa ini merupakan buku seri kedua yang berisi berbagai bahasan tentang pesawat pemancar dan carima radio dari yang sangat sederhana sampai yang relatif rumit Menggunakan buku ini secara bertahap pembaca akan diajak berkenalan berkelana berexperimen dan mencoba membuat sendiri berbagai macam pesawat pemancar atau carima radio Berbagai rangkaian elektronika dalam buku ini semuanya sudah dicoba dibuar dan diuji unjuk kerjanya di workshop penulis Buku ini bukanlah bukur teori melainkan buku yang bercerita tentang elektronika yang sebagian besar merupakan hasil experimen Karenanya pembaca tidak akan menemukan rumus rumus yang rumit Sebaliknya akan ditemukan gambar rangkaian elektronika foto gambar ilustrasi bahasan penjelasan

tabel nomogram cara pembuatan bahasan laporan unjuk kerja atau keterangan ringkas lainnya Karenanya buku sangat cocok untuk mereka yang ingin belajar elektronika tetapi tidak menyukai rumus atau perhitungan yang rumit Para siswa mahasiswa mereka yang tinggal dan bertugas jauh di pedalaman atau daerah terpencil para pendengar gelombang pendek SWL anggota amatir radio anggota KRAP CB er anggota militer atau polisi hobies serta teknisi radio atau teknisi komunikasi radio bisa menggunakan buku ini sebagai pedoman untuk membuat sendiri berbagai perangkat radio dan kelengkapannya

*Influence of Temperature on Microelectronics and System Reliability* Pradeep Lall, Michael G. Pecht, Edward B.

Hakim, 2020-07-09 This book raises the level of understanding of thermal design criteria It provides the design team with sufficient knowledge to help them evaluate device architecture trade offs and the effects of operating temperatures The author provides readers a sound scientific basis for system operation at realistic steady state temperatures without reliability penalties Higher temperature performance than is commonly recommended is shown to be cost effective in production for life cycle costs The microelectronic package considered in the book is assumed to consist of a semiconductor device with first level interconnects that may be wirebonds flip chip or tape automated bonds die attach substrate attach case lid lid seal and lead seal The temperature effects on electrical parameters of both bipolar and MOSFET devices are discussed and models quantifying the temperature effects on package elements are identified Temperature related models have been used to derive derating criteria for determining the maximum and minimum allowable temperature stresses for a given microelectronic package architecture The first chapter outlines problems with some of the current modeling strategies The next two chapters present microelectronic device failure mechanisms in terms of their dependence on steady state temperature temperature cycle temperature gradient and rate of change of temperature at the chip and package level Physics of failure based models used to characterize these failure mechanisms are identified and the variabilities in temperature dependence of each of the failure mechanisms are characterized Chapters 4 and 5 describe the effects of temperature on the performance characteristics of MOS and bipolar devices Chapter 6 discusses using high temperature stress screens including burn in for high reliability applications The burn in conditions used by some manufacturers are examined and a physics of failure approach is described The *Resources in Education*, 1991-04

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