



STM32F103xC STM32F103xD STM32F103xE

High-density performance line ARM-based 32-bit MCU with 256 to 512KB Flash, USB, CAN, 11 timers, 3 ADCs, 13 communication interfaces

Features

- Core: ARM 32-bit Cortex™-M3 CPU
 - 72 MHz maximum frequency, 1.25 DMIPS/MHz (Dhrystone 2.1) performance at 0 wait state memory access
 - Single-cycle multiplication and hardware division
- Memories
 - 256 to 512 Kbytes of Flash memory
 - up to 64 Kbytes of SRAM
 - Flexible static memory controller with 4 Chip Select. Supports Compact Flash, SRAM, PSRAM, NOR and NAND memories
 - LCD parallel interface, 8080/6800 modes
- Clock, reset and supply management
 - 2.0 to 3.6 V application supply and I/Os
 - POR, PDR, and programmable voltage detector (PVD)
 - 4-to-16 MHz crystal oscillator
 - Internal 8 MHz factory-trimmed RC
 - Internal 40 kHz RC with calibration
 - 32 kHz oscillator for RTC with calibration
- Low power
 - Sleep, Stop and Standby modes
 - V_{BAT} supply for RTC and backup registers
- 3 × 12-bit, 1 μs A/D converters (up to 21 channels)
 - Conversion range: 0 to 3.6 V
 - Triple-sample and hold capability
 - Temperature sensor
- 2 × 12-bit D/A converters
- DMA: 12-channel DMA controller
 - Supported peripherals: timers, A/Ds, DAC, SDIO, I²Ss, SPIs, I²Cs and USARTs
- Debug mode
 - Serial wire debug (SWD) & JTAG interfaces
 - Cortex-M3 Embedded Trace Macrocell™



- Up to 112 fast I/O ports
 - 51/80/112 I/Os, all mappable on 16 external interrupt vectors and almost all 5 V-tolerant
- Up to 11 timers
 - Up to four 16-bit timers, each with up to 4 IC/OC/PWM or pulse counter and quadrature (incremental) encoder input
 - 2 × 16-bit motor control PWM timers with dead-time generation and emergency stop
 - 2 × watch-dog timers (Independent and Window)
 - SysTick timer: a 24-bit downcounter
 - 2 × 16-bit basic timers to drive the DAC
- Up to 13 communication interfaces
 - Up to 2 × I²C interfaces (SMBus/PMBus)
 - Up to 5 USARTs (ISO 7816 interface, LIN, IrDA capability, modem control)
 - Up to 3 SPIs (18 Mbit/s), 2 with I²S interface multiplexed
 - CAN interface (2.0B Active)
 - USB 2.0 full speed interface
 - SDIO interface
- CRC calculation unit, 96-bit unique ID
- ECOMPACT® packages

Table 1. Device summary

Reference	Part number
STM32F103xC	STM32F103RC STM32F103VC STM32F1032C
STM32F103xD	STM32F103RD STM32F103VD STM32F1032D
STM32F103xE	STM32F103RE STM32F103ZE STM32F103VE

Stm32f102c4 Reference Manual

RS Peters



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Table of Contents Stm32f102c4 Reference Manual

1. Understanding the eBook Stm32f102c4 Reference Manual
 - The Rise of Digital Reading Stm32f102c4 Reference Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Stm32f102c4 Reference Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Stm32f102c4 Reference Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Stm32f102c4 Reference Manual
 - Personalized Recommendations
 - Stm32f102c4 Reference Manual User Reviews and Ratings
 - Stm32f102c4 Reference Manual and Bestseller Lists
5. Accessing Stm32f102c4 Reference Manual Free and Paid eBooks

- Stm32f102c4 Reference Manual Public Domain eBooks
- Stm32f102c4 Reference Manual eBook Subscription Services
- Stm32f102c4 Reference Manual Budget-Friendly Options
- 6. Navigating Stm32f102c4 Reference Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Stm32f102c4 Reference Manual Compatibility with Devices
 - Stm32f102c4 Reference Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Stm32f102c4 Reference Manual
 - Highlighting and Note-Taking Stm32f102c4 Reference Manual
 - Interactive Elements Stm32f102c4 Reference Manual
- 8. Staying Engaged with Stm32f102c4 Reference Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Stm32f102c4 Reference Manual
- 9. Balancing eBooks and Physical Books Stm32f102c4 Reference Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Stm32f102c4 Reference Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Stm32f102c4 Reference Manual
 - Setting Reading Goals Stm32f102c4 Reference Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Stm32f102c4 Reference Manual
 - Fact-Checking eBook Content of Stm32f102c4 Reference Manual
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

-
- Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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