

STM32F10xxx Flash memory microcontrollers

Introduction

This programming manual describes how to program the Flash memory of STM32F101xx, STM32F102xx, STM32F103xx, STM32F105xx and STM32F107xx microcontrollers. For convenience, these will be referred to as STM32F10xxx in the rest of this document unless otherwise specified.

The STM32F10xxx embedded Flash memory can be programmed using in-circuit programming or in-application programming.

The **in-circuit programming (ICP)** method is used to update the entire contents of the Flash memory, using the JTAG, SWD protocol or the boot loader to load the user application into the microcontroller. ICP offers quick and efficient design iterations and eliminates unnecessary package handling or socketing of devices.

In contrast to the ICP method, **in-application programming (IAP)** can use any communication interface supported by the microcontroller (I/Os, USB, CAN, UART, I²C, SPI, etc.) to download programming data into memory. IAP allows the user to re-program the Flash memory while the application is running. Nevertheless, part of the application has to have been previously programmed in the Flash memory using ICP.

The Flash interface implements instruction access and data access based on the AHB protocol. It implements a prefetch buffer that speeds up CPU code execution. It also implements the logic necessary to carry out Flash memory operations (Program/Erase). Program/Erase operations can be performed over the whole product voltage range. Read/Write protections and option bytes are also implemented.

[Table 1](#) lists the microcontrollers and evaluation tool concerned by this programming manual.

Table 1. Applicable products

Type	Applicable products
Microcontrollers	STM32F101xx, STM32F102xx, STM32F103xx, STM32F105xx and STM32F107xx microcontrollers
Evaluation tool	STM3210C-EVAL evaluation board

Stm32f10xxx Flash Programming Manual

Terry C. Jones



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Table of Contents Stm32f10xxx Flash Programming Manual

1. Understanding the eBook Stm32f10xxx Flash Programming Manual
 - The Rise of Digital Reading Stm32f10xxx Flash Programming Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Stm32f10xxx Flash Programming Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Stm32f10xxx Flash Programming Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Stm32f10xxx Flash Programming Manual
 - Personalized Recommendations
 - Stm32f10xxx Flash Programming Manual User Reviews and Ratings
 - Stm32f10xxx Flash Programming Manual and Bestseller Lists
5. Accessing Stm32f10xxx Flash Programming Manual Free and Paid eBooks
 - Stm32f10xxx Flash Programming Manual Public Domain eBooks
 - Stm32f10xxx Flash Programming Manual eBook Subscription Services
 - Stm32f10xxx Flash Programming Manual Budget-Friendly Options

6. Navigating Stm32f10xxx Flash Programming Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Stm32f10xxx Flash Programming Manual Compatibility with Devices
 - Stm32f10xxx Flash Programming Manual Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Stm32f10xxx Flash Programming Manual
 - Highlighting and Note-Taking Stm32f10xxx Flash Programming Manual
 - Interactive Elements Stm32f10xxx Flash Programming Manual
8. Staying Engaged with Stm32f10xxx Flash Programming Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Stm32f10xxx Flash Programming Manual
9. Balancing eBooks and Physical Books Stm32f10xxx Flash Programming Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Stm32f10xxx Flash Programming Manual
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Stm32f10xxx Flash Programming Manual
 - Setting Reading Goals Stm32f10xxx Flash Programming Manual
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Stm32f10xxx Flash Programming Manual
 - Fact-Checking eBook Content of Stm32f10xxx Flash Programming Manual
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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