

The background of the book cover is a high-resolution, colorized scanning electron micrograph (SEM) of a microchip die. The die is rectangular and densely packed with intricate circuit patterns. Different functional blocks are highlighted in various colors: large green rectangular areas, orange rectangular blocks, purple and blue regions, and yellow sections. A network of fine, dark lines represents the interconnects or routing across the surface. The overall appearance is that of a complex, multi-layered electronic circuit.

FOURTH EDITION

CMOS VLSI DESIGN

**A CIRCUITS
AND
SYSTEMS
PERSPECTIVE**

NEIL H. E. WESTE

DAVID MONEY HARRIS

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