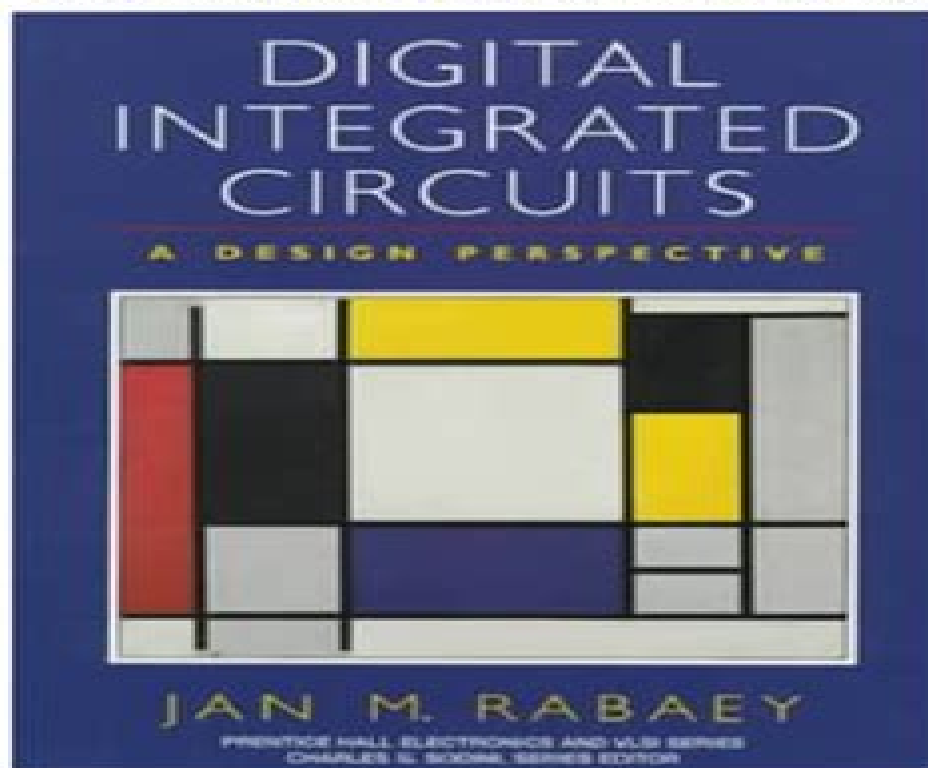


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Smart Multicore Embedded Systems

Massimo Torquati, Koen Bertels, Sven Karlsson, François Pacull, 2013-11-09 This book provides a single source reference to the state of the art of high level programming models and compilation tool chains for embedded system platforms The authors address challenges faced by programmers developing software to implement parallel applications in embedded systems where very often they are forced to rewrite sequential programs into parallel software taking into account all the low level features and peculiarities of the underlying platforms Readers will benefit from these authors approach which takes into account both the application requirements and the platform specificities of various embedded systems from different industries Parallel programming tool chains are described that take as input parameters both the application and the platform model then determine relevant transformations and mapping decisions on the concrete platform minimizing user intervention and hiding the difficulties related to the correct and efficient use of memory hierarchy and low level code generation

The VLSI Handbook

Wai-Kai Chen, 2018-10-03 For the new millenium Wai Kai Chen introduced a monumental reference for the design analysis and prediction of VLSI circuits The VLSI Handbook Still a valuable tool for dealing with the most dynamic field in engineering this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts models and equations Written by a stellar international panel of expert contributors this handbook is a reliable comprehensive resource for real answers to practical problems It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus WHAT'S IN THE SECOND EDITION Sections on Low power electronics and design VLSI signal processing Chapters on CMOS fabrication Content addressable memory Compound semiconductor RF

circuits High speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions The VLSI Handbook Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice Low Power Design Methodologies Jan M. Rabaey, Massoud Pedram, 2012-12-06 Low Power Design Methodologies presents the first in depth coverage of all the layers of the design hierarchy ranging from the technology circuit logic and architectural levels up to the system layer The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction Finally it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments The individual chapters are written by the leading researchers in the area drawn from both industry and academia Extensive references are included at the end of each chapter Audience A broad introduction for anyone interested in low power design Can also be used as a text book for an advanced graduate class A starting point for any aspiring researcher **Low Power Interconnect Design** Sandeep Saini, 2015-06-12 This book provides practical solutions for delay and power reduction for on chip interconnects and buses It provides an in depth description of the problem of signal delay and extra power consumption possible solutions for delay and glitch removal while considering the power reduction of the total system Coverage focuses on use of the Schmitt Trigger as an alternative approach to buffer insertion for delay and power reduction in VLSI interconnects In the last section of the book various bus coding techniques are discussed to minimize delay and power in address and data buses Regular Nanofabrics in Emerging Technologies M. Haykel Ben Jamaa, 2011-03-24 Regular Nanofabrics in Emerging Technologies gives a deep insight into both fabrication and design aspects of emerging semiconductor technologies that represent potential candidates for the post CMOS era Its approach is unique across different fields and it offers a synergetic view for a public of different communities ranging from technologists to circuit designers and computer scientists The book presents two technologies as potential candidates for future semiconductor devices and systems and it shows how fabrication issues can be addressed at the design level and vice versa The reader either for academic or research purposes will find novel material that is explained carefully for both experts and non initiated readers Regular Nanofabrics in Emerging Technologies is a survey of post CMOS technologies It explains processing circuit and system level design for people with various backgrounds Analysis and Design of Resilient VLSI Circuits Rajesh Garg, 2009-10-22 This monograph is motivated by the challenges faced in designing reliable VLSI systems in modern VLSI processes The reliable operation of integrated circuits ICs has become increasingly difficult to achieve in the deep submicron DSM era With continuously decreasing device feature sizes combined with lower supply voltages and higher operating frequencies the noise immunity of VLSI circuits is decreasing alarmingly Thus VLSI circuits are becoming more vulnerable to noise effects such as crosstalk power supply variations and radiation induced soft errors

Among these noise sources soft errors or error caused by radiation particle strikes have become an increasingly troublesome issue for memory arrays as well as combinational logic circuits. Also in the DSM era process variations are increasing at a significant rate making it more difficult to design reliable VLSI circuits. Hence it is important to efficiently design robust VLSI circuits that are resilient to radiation particle strikes and process variations. The work presented in this research monograph presents several analysis and design techniques with the goal of realizing VLSI circuits which are radiation and process variation tolerant.

Efficient Design of Variation-Resilient Ultra-Low Energy Digital Processors Hans Reyserhove, Wim Dehaene, 2019-03-27 This book enables readers to achieve ultra low energy digital system performance. The author's main focus is the energy consumption of microcontroller architectures in digital sub systems. The book covers a broad range of topics extensively from circuits through design strategy to system architectures. The result is a set of techniques and a context to realize minimum energy digital systems. Several prototype silicon implementations are discussed which put the proposed techniques to the test. The achieved results demonstrate an extraordinary combination of variation resilience, high speed performance and ultra low energy.

The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering. The unparalleled rate of technological advancement, the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own. References published only a few years ago are now sorely out of date. The Computer Engineering Handbook changes all of that. Under the leadership of Vojin Oklobdzija and a stellar editorial board, some of the industry's foremost experts have joined forces to create what promises to be the definitive resource for computer design and engineering. Instead of focusing on basic introductory material, it forms a comprehensive state of the art review of the field's most recent achievements, outstanding issues and future directions. The world of computer engineering is vast and evolving so rapidly that what is cutting edge today may be obsolete in a few months. While exploring the new developments, trends and future directions of the field, The Computer Engineering Handbook captures what is fundamental and of lasting value.

Wireless Communications Circuits and Systems Institution of Electrical Engineers, 2004 This book examines integrated circuits, systems and transceivers for wireless and mobile communications. It covers the most recent developments in key RF/IF analogue mixed signal components and single chip transceivers in CMOS technology.

Continuous-Time Digital Front-Ends for Multistandard Wireless Transmission Pieter A. J. Nuyts, Patrick Reynaert, Wim Dehaene, 2014-01-03 This book describes the design of fully digital multistandard transmitter front ends which can directly drive one or more switching power amplifiers, thus eliminating all other analog components. After reviewing different architectures, the authors focus on polar architectures using pulse width modulation (PWM) which are entirely based on unclocked delay lines and other continuous time digital hardware. As a result, readers are enabled to shift accuracy concerns from the voltage domain to the time domain.

to coincide with submicron CMOS technology scaling The authors present different architectural options and compare them based on their effect on the signal and spectrum quality Next a high level theoretical analysis of two different PWM based architectures baseband PWM and RF PWM is made On the circuit level traditional digital components and design techniques are revisited from the point of view of continuous time digital circuits Important design criteria are identified and different solutions are presented along with their advantages and disadvantages Finally two chips designed in nanometer CMOS technologies are described along with measurement results for validation

Ultra-Low-Voltage Design of Energy-Efficient Digital Circuits Nele Reynders, Wim Dehaene, 2015-04-14 This book focuses on increasing the energy efficiency of electronic devices so that portable applications can have a longer stand alone time on the same battery The authors explain the energy efficiency benefits that ultra low voltage circuits provide and provide answers to tackle the challenges which ultra low voltage operation poses An innovative design methodology is presented verified and validated by four prototypes in advanced CMOS technologies These prototypes are shown to achieve high energy efficiency through their successful functionality at ultra low supply voltages

Proceedings of International Conference on VLSI, Communication, Advanced Devices, Signals & Systems and Networking (VCASAN-2013) Veena S. Chakravarthi, Yasha Jyothi M. Shirur, Rekha P., 2013-07-10 This book is a collection of papers presented by renowned researchers keynote speakers and academicians in the International Conference on VLSI Communication Analog Designs Signals Systems and Networking VCASAN 2013 organized by B N M Institute of Technology Bangalore India during July 17 19 2013 The book provides global trends in cutting edge technologies in electronics and communication engineering The content of the book is useful to engineers researchers and academicians as well as industry professionals

Introduction to VLSI Circuits and Systems John P. Uyemura, 2002 CD ROM contains AIM SPICE from AIM Software Micro Cap 6 from Spectrum Software Silos III Verilog Simulator from Simucad Adobe Acrobat Reader 4.0 from Adobe

Test and Diagnosis for Small-Delay Defects Mohammad Tehranipoor, Ke Peng, Krishnendu Chakrabarty, 2011-09-08 This book will introduce new techniques for detecting and diagnosing small delay defects in integrated circuits Although this sort of timing defect is commonly found in integrated circuits manufactured with nanometer technology this will be the first book to introduce effective and scalable methodologies for screening and diagnosing small delay defects including important parameters such as process variations crosstalk and power supply noise

Timing Performance of Nanometer Digital Circuits Under Process Variations Victor Champac, Jose Garcia Gervacio, 2018-04-18 This book discusses the digital design of integrated circuits under process variations with a focus on design time solutions The authors describe a step by step methodology going from logic gates to logic paths to the circuit level Topics are presented in comprehensively without overwhelming use of analytical formulations Emphasis is placed on providing digital designers with understanding of the sources of process variations their impact on circuit performance and tools for improving their designs to comply with product specifications Various circuit level design hints are

highlighted so that readers can use them to improve their designs. A special treatment is devoted to unique design issues and the impact of process variations on the performance of FinFET based circuits. This book enables readers to make optimal decisions at design time toward more efficient circuits with better yield and higher reliability. Introduction to VLSI Systems Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost. With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding. *Model and Design of Improved Current Mode Logic Gates* Kirti Gupta, Neeta Pandey, Maneesha Gupta, 2019-11-22 This book presents MOSFET based current mode logic CML topologies which increase the speed and lower the transistor count, supply voltage and power consumption. The improved topologies modify the conventional PDN load and the current source sections of the basic CML gates. Electronic system implementation involves embedding digital and analog circuits on a single die shifting towards mixed mode circuit design. The high resolution low power and low voltage analog circuits are combined with high frequency complex digital circuits and the conventional static CMOS logic generates large current spikes during the switching also referred to as digital switching noise which degrades the resolution of the sensitive analog circuits via supply line and substrate coupling. This problem is exacerbated further with scaling down of CMOS technology due to higher integration levels and operating frequencies. In the literature several methods are described to reduce the propagation of the digital switching noise. However in high resolution applications these methods are not sufficient. The conventional CMOS static logic is no longer an effective solution and therefore an alternative with reduced current spikes or that draws a constant supply current must be selected. The current mode logic CML topology with its unique property of requiring constant supply current is a promising alternative to the conventional CMOS static logic.

Wearable Sensors Edward Sazonov, 2020-11-10 *Wearable Sensors Fundamentals Implementation and Applications* has been written by a collection of experts in their field who each provide you with an understanding of how to design and work with wearable sensors. Together these insights provide the first single source of information on wearable sensors that would be a fantastic addition to the library of any engineers working in this field. *Wearable Sensors* covers a wide variety of topics associated with development and applications of wearable sensors. It also provides an overview and a coherent summary of many aspects of wearable sensor technology. Both professionals in industries and academic researchers need this package of information in order to learn the overview and each specific technology at the same time. This book includes the most current knowledge on the advancement of light weight hardware energy harvesting signal processing and wireless communications and networks. Practical problems with smart fabrics biomonitors and health informatics are all addressed plus end user centric design ethical and safety issues. The new edition is completely reviewed by key figures in the field who offer authoritative and comprehensive information on the various topics. A new feature for the second edition is the incorporation

of key background information on topics to allow the less advanced user access to the field and to make the title more of an auto didactic book for undergraduates Provides a full revision of the first edition providing a comprehensive and up to date resource of all currently used wearable devices in an accessible and structured manner Helps engineers manufacture wearable devices with information on current technologies with a focus on end user needs and recycling requirements This book provides a fully updated overview of the many aspects of wearable sensor technology in one single volume enabling engineers and researchers to fully comprehend the field and to identify opportunities

Hardware-Aware Probabilistic Machine Learning Models Laura Isabel Galindez Olascoaga, Wannes Meert, Marian Verhelst, 2021-05-19 This book proposes probabilistic machine learning models that represent the hardware properties of the device hosting them These models can be used to evaluate the impact that a specific device configuration may have on resource consumption and performance of the machine learning task with the overarching goal of balancing the two optimally The book first motivates extreme edge computing in the context of the Internet of Things IoT paradigm Then it briefly reviews the steps involved in the execution of a machine learning task and identifies the implications associated with implementing this type of workload in resource constrained devices The core of this book focuses on augmenting and exploiting the properties of Bayesian Networks and Probabilistic Circuits in order to endow them with hardware awareness The proposed models can encode the properties of various device sub systems that are typically not considered by other resource aware strategies bringing about resource saving opportunities that traditional approaches fail to uncover The performance of the proposed models and strategies is empirically evaluated for several use cases All of the considered examples show the potential of attaining significant resource saving opportunities with minimal accuracy losses at application time Overall this book constitutes a novel approach to hardware algorithm co optimization that further bridges the fields of Machine Learning and Electrical Engineering

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