Goals of Ultra DMA

Higher transfer rate

 Increasing interface transfer rate keeps the interface from becoming a bottleneck for system performance as disk read channel rates increase.

Retain or improve reliability

- False transitions on edges of control signals (e.g. DIOR-/DIOW-)
 can prevent systems from working at any speed.
- Achieved through series termination and CRC checking.

Low cost implementation

 Achieved by minimizing changes required to support protocol and through backward compatibility.

Continue to allow simple end-user configuration.

- No hardware changes for Ultra 33
- New cable required for modes higher than 2 is only system configuration change seen by user.



Ultra Dma Implementation Guide

Mike Meyers, Scott Jernigan, Daniel Lachance

Ultra Dma Implementation Guide:

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