

Model Question Paper- I with effect from 2022

CBCS SCHEME

First/ Second Semester B.E Degree Examination, _____

Basic Electronics (BBEE103/203)

TIME: 03 Hours

Max.Marks:100

Notes:

1. Answer any FIVE full questions, choosing at least ONE question from each MODULE
2. VTU Formula Hand Book is Permitted
3. M: Marks, L: Bloom's level, C: Course outcomes.

		Module - 1	M	L	C
Q.1	a	Explain forward and reverse biasing VI characteristics of PN junction.	6	L2	CO1
	b	A Zener diode has a breakdown voltage of 10V. it is supplied from a voltage source varying between 20 to 40 V in series with a resistance of 820 Ω . Using ideal Zener diode model obtain minimum and maximum zener currents.	7	L2	CO1
	c	With neat circuit diagram and waveforms explain the working of bridge rectifier.	7	L2	CO1
OR					
Q.2	a	Explain how zener diode can be used as a voltage regulator.	6	L2	CO1
	b	Explain with neat diagram and waveform working of the half wave rectifier and also derive an expression for the efficiency and ripple factor	7	L2	CO1
	c	In a full-wave rectifier, the input is from 30-0-30 V transformer. The load and diode forward resistance are 100 Ω and 10 Ω respectively. Calculate the average load current, average load voltage and rectifier efficiency.	7	L2	CO1
Module – 2					
Q.3	a	Explain input and output characteristics of the common emitter configuration	6	L2	CO2
	b	Make use of N-channel enhancement type MOSFET and describe the construction and working.	7	L2	CO2
	c	Explain various currents and voltages flowing through the BJT transistor.	7	L3	CO2
OR					
Q.4	a	Explain the transistor biasing circuit with relevant expressions.	7	L2	CO2
	b	Make use of n-channel JFET to describe its operation and characteristics.	7	L2	CO2
	c	Define α and β . Determine the relationship between α and β	6	L2	CO2

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RD Boyd



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