

during positive half cycle. Thus, during the negative half cycle

- a) $V_o = V_r$
- b) $V_o = 0$
- c) $V_o = V_{dc} + V_r$
- d) $V_o = V_{dc}$

8. When a diode is connected in series with an AC source & R load, the conduction time per cycle is

- a) 0
- b) 2π
- c) π
- d) $\pi/2$

9. When diode is connected in series to an AC source & RL load, the conduction time for the diode

- a) is always less than π
- b) is 0
- c) is π
- d) can be greater than π

10. For the circuit shown below,

$$V_{dc} = 50 \text{ V}$$

$$\text{Cut-in voltage for } D1 = 0.2 \text{ V}$$

$$\text{Cut-in voltage for } D2 = 0.6 \text{ V}$$

$$R = 5 \text{ K}\Omega$$

Current through $D1$ & $D2$ would be,

- a) 5mA, 5mA
- b) 10mA, 0
- c) 0, 10mA
- d) 9.98mA, 9.94mA

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