Practical FPGA Programming in C

Presents C-based techniques for building high-performance, FPGA-accelerated software applications

David Pellerin • Scott Thibault

Foreword by Clive "Max" Maxfield

Practical Fpga Programming In C

Alessandro De Angelis,Oriana Mansutti

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    Practical FPGA Programming in C David Pellerin,Scott Thibault,2005 FPGA brings high performance applications to market quickly this book covers the many emerging platforms in a proven effective manner **Behavioral Modeling for Embedded Systems and Technologies: Applications for Design and Implementation** Gomes, Luis,Fernandes, Jo?o M.,2009-07-31 This book provides innovative behavior models currently used for developing embedded systems accentuating on graphical and visual notations Provided by publisher Timing Analysis and Simulation for Signal Integrity Engineers Greg Edlund,2007-10-22 Every day companies call upon their signal integrity engineers to make difficult decisions about design constraints and timing margins Can I move these wires closer together How many holes can I drill in this net How far apart can I place these chips Each design is unique there s no single recipe that answers all the questions Today s designs require ever greater precision but design guides for specific digital interfaces are by nature conservative Now for the first time there s a complete guide to timing analysis and simulation that will help you manage the tradeoffs between signal integrity performance and cost Writing from the perspective of a practicing SI engineer and team lead Greg Edlund of IBM presents deep knowledge and quantitative techniques for making better decisions about digital interface design Edlund shares his insights into how and why digital interfaces fail revealing how fundamental sources of pathological effects can combine to create fault conditions You won t just learn Edlund s expert techniques for avoiding failures you ll learn how to develop the right approach for your own projects and environment Coverage includes Systematically ensure that interfaces will operate with positive timing margin over the product s lifetime without incurring excess cost Understand essential chip to chip timing concepts in the context of signal integrity Collect the right information upfront so you can analyze new designs more effectively Review the circuits that store information in CMOS state machines and how they fail Learn how to time common clock source synchronous and high speed serial transfers Thoroughly understand how interconnect electrical characteristics affect timing propagation delay impedance profile crosstalk resonances and frequency dependent loss Model 3D discontinuities using electromagnetic field solvers Walk through four case studies coupled differential vias land grid array connector DDR2 memory data transfer and PCI Express channel Appendices present a refresher on SPICE modeling and a high level conceptual framework for electromagnetic field behavior Objective realistic and practical this is the signal integrity resource engineers have been searching for Preface xiii Acknowledgments xvi About the Author xix About the Cover xx Chapter 1 Engineering Reliable Digital Interfaces 1 Chapter 2 Chip to Chip Timing 13 Chapter 3 Inside IO Circuits 39 Chapter 4 Modeling 3D Discontinuities 73 Chapter 5 Practical 3D Examples 101 Chapter 6 DDR2 Case Study 133 Chapter 7 PCI Express Case Study 175 Appendix A A Short CMOS and SPICE Primer 209 Appendix B A Stroll Through 3D Fields 219 Endnotes 233 Index 235 Modern VLSI Design Wayne Wolf,2008-12-21 The Number 1 VLSI Design Guide Now Fully Updated for IP Based Design and the Newest Technologies Modern VLSI Design Fourth Edition offers authoritative up to the

minute guidance for the entire VLSI design process from architecture and logic design through layout and packaging Wayne Wolf has systematically updated his award winning book for today s newest technologies and highest value design techniques Wolf introduces powerful new IP based design techniques at all three levels gates subsystems and architecture He presents deeper coverage of logic design fundamentals clocking and timing and much more No other VLSI guide presents as much up to date information for maximizing performance minimizing power utilization and achieving rapid design turnarounds   *A Practical Introduction to Hardware/Software Codesign* Patrick R. Schaumont,2010-09-09 This is a practical book for computer engineers who want to understand or implement hardware software systems It focuses on problems that require one to combine hardware design with software design such problems can be solved with hardware software codesign When used properly hardware software co sign works better than hardware design or software design alone it can improve the overall performance of digital systems and it can shorten their design time Hardware software codesign can help a designer to make trade offs between the exibility and the performanceof a digital system To achieve this a designer needs to combine two radically different ways of design the sequential way of dec position in time using software with the parallel way of decomposition in space using hardware Intended Audience This book assumes that you have a basic understandingof hardware that you are miliar with standard digital hardware componentssuch as registers logic gates and components such as multiplexers and arithmetic operators The book also assumes that you know how to write a program in C These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering   Transition of HPC Towards Exascale Computing E.H. D'Hollander,2013-10-22 The US Europe Japan and China are racing to develop the next generation of supercomputers exascale machines capable of 10 to the 18th power calculations a second by 2020 But the barriers are daunting the challenge is to change the paradigm of high performance computing The 2012 biennial high performance workshop held in Cetraro Italy in June 2012 focused on the challenges facing the computing research community to reach exascale performance in the next decade This book presents papers from this workshop arranged into four major topics energy scalability new architectural concepts and programming of heterogeneous computing systems Chapter 1 introduces the status of present supercomputers which are still about two orders of magnitude separated from the exascale mark Chapter 2 examines energy demands a major limiting factor of today s fastest supercomputers the quantum leap in performance required for exascale computing will require a shift in architectures and technology In Chapter 3 scalable computer paradigms for dense linear algebra on massive heterogeneous systems are presented and Chapter 4 discusses architectural concepts Finally Chapter 5 addresses the programming of heterogeneous systems This book will be of interest to all those wishing to understand how the development of modern supercomputers is set to advance in the next decade   **Power Integrity Modeling and Design for Semiconductors and Systems** Madhavan Swaminathan,Ege Engin,2007-11-19 The First Comprehensive Example Rich Guide to Power Integrity Modeling

Professionals such as signal integrity engineers package designers and system architects need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems Now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art Using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise The authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications Their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications The authors Introduce power delivery network components analysis high frequency measurement and modeling requirements Thoroughly explain modeling of power ground planes including plane behavior lumped modeling distributed circuit based approaches and much more Offer in depth coverage of simultaneous switching noise including modeling for return currents using time and frequency domain analysis Introduce several leading time domain simulation methods such as macromodeling and discuss their advantages and disadvantages Present the application of the modeling methods on several advanced case studies that include high speed servers high speed differential signaling chip package analysis materials characterization embedded decoupling capacitors and electromagnetic bandgap structures This book s system level focus and practical examples will make it indispensable for every student and professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists It will also be valuable to developers building software that helps to analyze high speed systems **Signal and Power Integrity--simplified** Eric Bogatin,2010 With the inclusion of the two new hot topics in signal integrity power integrity and high speed serial links this book will be the most up to date complete guide to understanding and designing for signal integrity Design for Embedded Image Processing on FPGAs Donald G. Bailey,2011-06-13 Dr Donald Bailey starts with introductory material considering the problem of embedded image processing and how some of the issues may be solved using parallel hardware solutions Field programmable gate arrays FPGAs are introduced as a technology that provides flexible fine grained hardware that can readily exploit parallelism within many image processing algorithms A brief review of FPGA programming languages provides the link between a software mindset normally associated with image processing algorithms and the hardware mindset required for efficient utilization of a parallel hardware design The design process for implementing an image processing algorithm on an FPGA is compared with that for a conventional software implementation with the key differences highlighted Particular attention is given to the techniques for mapping an algorithm onto an FPGA implementation considering timing memory bandwidth and resource constraints and efficient hardware computational techniques Extensive coverage is given of a range of low and intermediate level image processing operations discussing efficient implementations and how these may vary according to the application

The techniques are illustrated with several example applications or case studies from projects or applications he has been involved with Issues such as interfacing between the FPGA and peripheral devices are covered briefly as is designing the system in such a way that it can be more readily debugged and tuned Provides a bridge between algorithms and hardware Demonstrates how to avoid many of the potential pitfalls Offers practical recommendations and solutions Illustrates several real world applications and case studies Allows those with software backgrounds to understand efficient hardware implementation Design for Embedded Image Processing on FPGAs is ideal for researchers and engineers in the vision or image processing industry who are looking at smart sensors machine vision and robotic vision as well as FPGA developers and application engineers The book can also be used by graduate students studying imaging systems computer engineering digital design circuit design or computer science It can also be used as supplementary text for courses in advanced digital design algorithm and hardware implementation and digital signal processing and applications Companion website for the book www wiley com go bailey fpga **Reconfigurable Computing: Architectures, Tools and Applications** Andreas Koch,Ram Krishnamurthy,John McAllister,Roger Woods,Tarek El-Ghazawi,2011-03-15 This book constitutes the refereed proceedings of the 7th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2011 held in Belfast UK in March 2011 The 40 revised papers presented consisting of 24 full papers 14 poster papers and the abstracts of 2 plenary talks were carefully reviewed and selected from 88 submissions The topics covered are reconfigurable accelerators design tools reconfigurable processors applications device architecture methodology and simulation and system architecture **E-Learning** Elvis Pontes,Adilson Guelfi,Sergio Kofuji,2012-03-14 E learning enables students to pace their studies according to their needs making learning accessible to 1 people who do not have enough free time for studying they can program their lessons according to their available schedule 2 those far from a school geographical issues or the ones unable to attend classes due to some physical or medical restriction Therefore cultural geographical and physical obstructions can be removed making it possible for students to select their path and time for the learning course Students are then allowed to choose the main objectives they are suitable to fulfill This book regards E learning challenges opening a way to understand and discuss questions related to long distance and lifelong learning E learning for people with special needs and lastly presenting case study about the relationship between the quality of interaction and the quality of learning achieved in experiences of E learning formation **Principles and Structures of FPGAs** Hideharu Amano,2018-09-03 This comprehensive textbook on the field programmable gate array FPGA covers its history fundamental knowledge architectures device technologies computer aided design technologies design tools examples of application and future trends Programmable logic devices represented by FPGAs have been rapidly developed in recent years and have become key electronic devices used in most IT products This book provides both complete introductions suitable for students and beginners and high level techniques useful for engineers and researchers in this field Differently developed from usual

integrated circuits the FPGA has unique structures design methodologies and application techniques Allowing programming by users the device can dramatically reduce the rising cost of development in advanced semiconductor chips The FPGA is now driving the most advanced semiconductor processes and is an all in one platform combining memory CPUs and various peripheral interfaces This book introduces the FPGA from various aspects for readers of different levels Novice learners can acquire a fundamental knowledge of the FPGA including its history from Chapter 1 the first half of Chapter 2 and Chapter 4 Professionals who are already familiar with the device will gain a deeper understanding of the structures and design methodologies from Chapters 3 and 5 Chapters 6 8 also provide advanced techniques and cutting edge applications and trends useful for professionals Although the first parts are mainly suitable for students the advanced sections of the book will be valuable for professionals in acquiring an in depth understanding of the FPGA to maximize the performance of the device

Theoretical and Mathematical Foundations of Computer Science Qihai Zhou,2011-10-29 This book constitutes the refereed post proceedings of the Second International Conference on Theoretical and Mathematical Foundations of Computer Science ICTMF 2011 held in Singapore in May 2011 The conference was held together with the Second International Conference on High Performance Networking Computing and Communication systems ICHCC 2011 which proceedings are published in CCIS 163 The 84 revised selected papers presented were carefully reviewed and selected for inclusion in the book The topics covered range from computational science engineering and technology to digital signal processing and computational biology to game theory and other related topics   Compiled Acceleratin of C Programs on FPGAs Jason Richard Villarreal,2008   Computational Science and Its Applications - ICCSA 2010 David Taniar,Osvaldo Gervasi,Beniamino Murgante,Eric Pardede,Bernady O. Apduhan,2010-04-03 These multiple volumes LNCS volumes 6016 6017 6018 and 6019 consist of the peer reviewed papers from the 2010 International Conference on Compu tional Science and Its Applications ICCSA2010 held in Fukuoka Japanduring March23 26 2010 ICCSA2010wasasuccessfuleventintheInternationalC ferences on Computational Science and Its Applications ICCSA conference ries previouslyheld in Suwon South Korea 2009 Perugia Italy 2008 Kuala Lumpur Malaysia 2007 Glasgow UK 2006 Singapore 2005 Assisi Italy 2004 Montreal Canada 2003 and as ICCS Amsterdam The Netherlands 2002 and San Francisco USA 2001 Computational science is a main pillar of most of the present research dustrial and commercial activities and plays a unique role in exploiting ICT novative technologies The ICCSA conference series has been providing a venue to researchers and industry practitioners to discuss new ideas to share complex problems and their solutions and to shape new trends in computational science ICCSA 2010 was celebrated at the host university Kyushu Sangyo Univ sity Fukuoka Japan as part of the university s 50th anniversary We would like to thank Kyushu Sangyo University for hosting ICCSA this year and for cluding this international event in their celebrations Also for the rst time this year ICCSA organized poster sessions that present on going projects on various aspects of computational sciences   *VLSI 2010 Annual Symposium* Nikolaos

Voros,Amar Mukherjee,Nicolas Sklavos,Konstantinos Masselos,Michael Huebner,2011-09-08 VLSI 2010 Annual Symposium will present extended versions of the best papers presented in ISVLSI 2010 conference The areas covered by the papers will include among others Emerging Trends in VLSI Nanoelectronics Molecular Biological and Quantum Computing MEMS VLSI Circuits and Systems Field programmable and Reconfigurable Systems System Level Design System on a Chip Design Application Specific Low Power VLSI System Design System Issues in Complexity Low Power Heat Dissipation Power Awareness in VLSI Design Test and Verification Mixed Signal Design and Analysis Electrical Packaging Co Design Physical Design Intellectual property creating and sharing **Proceedings of the Third Workshop on Science with the New Generation of High Energy Gamma-ray Experiments** Alessandro De Angelis,Oriana Mansutti,2006 Introduction I Detectors for high energy gamma rays First results from the MAGIC experiment D Bastieri for the MAGIC collaboration H E S S P Vincent for the H E S S collaboration CANGAROO M Mori for the CANGAROO II III Team The status of VERITAS M K Daniel on behalf of the VERITAS collaboration Gamma ray bursts recent results obtained by the SWIFT mission G Chinearini on behalf of the SWIFT team Functional tests and performance characterization during the assembly phase of the modules of the AGILE silicon tracker M Basset und weitere Status of GLAST the gamma ray large area space telescope L Rochester on behalf of the GLAST team Status of the ARGO YBJ experiment P Camarri for the ARGO YBJ collaboration Gamma Air Watch GAW an imaging atmospheric Cherenkov telescope large with large field of view T Mineo und weitere II Topics in fundamental physics Frontiers of high energy cosmic rays M Pimenta Measurement of cosmological parameters A Balbi The present and the future of cosmology with gamma ray bursts G Ghirlanda G Ghisellini Supersymmetry breaking extra dimensions and neutralino dark matter A M Lionetto Dark matter at symbol rays L Pieri Populations of subhalos in cold dark matter halos E Bisesi III Multiwavelength observations WEBT multifrequency support to space observations C M Raiteri and M Villata for the WEBT collaboration REM The Remote Observatory for GRB et al E Molinari on behalf of the REM ROSS team Planck LFI operation of the scientific ground segment F Pasian und weitere INTEGRAL three years later L Foschini G Di Cocco G Malaguti XMM observations of Geminga PSR B1055 52 and PSR B0656 14 phase resolved spectroscopy as a tool to investigate the X symbol connection P A Caraveo und weitere IV Poster session Software time calibration of the ARGO YBJ detector A K Calabrese Melcarne for the ARGO YBJ collaboration Gamma ray burst physics with GLAST N Omodei Observations of blazars and EGRET sources with INTEGRAL V Vitale und weitere A third level trigger programmable on FPGA for the gamma hadron separation in a Cherenkov telescope using Pseudo Zernike moments and the SVM classifier M Frailis und weitere PulsarSpectrum simulating gamma ray pulsars for the GLAST mission M Razzano und weitere **Computational Intelligence: A Compendium** John Fulcher,L. C. Jain,2008-06-16 Computational Intelligence A Compendium presents a well structured overview about this rapidly growing field with contributions of leading experts in Computational Intelligence The main focus of the compendium is on applied methods tired and proven effective to realworld

problems which is especially useful for practitioners researchers students and also newcomers to the field The 25 chapters are grouped into the following themes I Overview and Background II Data Preprocessing and Systems Integration III Artificial Intelligence IV Logic and Reasoning V Ontology VI Agents VII Fuzzy Systems VIII Artificial Neural Networks IX Evolutionary Approaches X DNA and Immune based Computing   <u>Compilation and Synthesis for Embedded Reconfigurable Systems</u> João Manuel Paiva Cardoso,Pedro C. Diniz,José Gabriel de Figueiredo Coutinho,Zlatko Marinov Petrov,2013-05-16 This book provides techniques to tackle the design challenges raised by the increasing diversity and complexity of emerging heterogeneous architectures for embedded systems It describes an approach based on techniques from software engineering called aspect oriented programming which allow designers to control today s sophisticated design tool chains while maintaining a single application source code Readers are introduced to the basic concepts of an aspect oriented domain specific language that enables control of a wide range of compilation and synthesis tools in the partitioning and mapping of an application to a heterogeneous and possibly multi core target architecture Several examples are presented that illustrate the benefits of the approach developed for applications from avionics and digital signal processing Using the aspect oriented programming techniques presented in this book developers can reuse extensive sections of their designs while preserving the original application source code thus promoting developer productivity as well as architecture and performance portability Describes an aspect oriented approach for the compilation and synthesis of applications targeting heterogeneous embedded computing architectures Includes examples using an integrated tool chain for compilation and synthesis Provides validation and evaluation for targeted reconfigurable heterogeneous architectures Enables design portability given changing target devices Allows developers to maintain a single application source code when targeting multiple architectures   **The Industrial Electronics Handbook - Five Volume Set** Bogdan M. Wilamowski,J. David Irwin,2011-03-04 Industrial electronics systems govern so many different functions that vary in complexity from the operation of relatively simple applications such as electric motors to that of more complicated machines and systems including robots and entire fabrication processes The Industrial Electronics Handbook Second Edition combines traditional and new

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