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Third Semester B.E. Degree Examination, Aug./Sept.2020

Computer Organization and Architecture

Time: 3 hrs. Max. Marks: 100 Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

1	a.	Explain the operation of computer with neat block diagram.	(10 Marks)
	b.	Explain computer basic performance equation.	(04 Marks)

- Explain following with an example: i) Three address instruction
 - ii) Two address instruction iii) One address instruction.

- Explain Single -BUS structure in computer.
 - Explain system software functions in computer.
 - What is Operating system? Explain user program and OS routine sharing the processor.
 - (08 Marks)

Module-2

- Explain Big Endian and Little-Endian with neat diagram. 3
 - Explain memory operations with examples.
 - Explain condition codes with examples.

- Discuss following addressing modes with example:
 - i) Immediate ii) Register iii) Direct iv) Indirect b. What are assembler directive? Explain any five assembler directives.

Module-3

- With a neat diagram, explain how to interface printer to the processor.
 - Define Interrupt. Point out and explain the various ways of enabling and disabling interrupts. (10 Marks)

- Explain the following method of handling interrupts from multiple devices.
 - i) Daisy chain method ii) Priority structure.
 - Explain operation of DMA with neat diagram. ь.

Module-4

- Explain internal organization of 16 × 8 memory chip.
 - Discuss a single-transistor dynamic memory cell.
 - Write a note on Virtual Memory.

- Draw and explain the internal organization of 2M × 8 asyndronous DRAM Chip. a.
 - Describe the principles of magnetic disk. b.
 - What is mapping? Explain set associative cache mapping techniques. C.

Module-5

- Discuss with neat diagram, the single bus organization of data path inside a processor. (10 Marks)
 - What are the action required to execute a complete instruction ADD(R3), R1.

- Draw and explain multiple bus organization of CPU. 10
 - (10 Marks) Draw and explain organization of the control unit to allow conditional branching in the (10 Marks) microprogram.

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