Multiple Choice Questions and Answers on Logic Gates

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Sec. 15.	The universal gate is
	NAND caste
	OR gate
	AND gate
	None of the above
Ans	
	The inverter is
	NOT gate
	OR gate
	AND gate
	None of the above
Aures.	
	The inputs of a NAND gate are connected together. The resulting circuit is
	OR gate
	AND gate
	NOT gate
	None of the above
Aures	. 3
Q4.	The NOR gate is OR gate followed by
3.0	AND gate
2.	
33.7	NOT gate
44.	None of the above
Aures.	_ 3
Q5.	The NAND gate is AND gate followed by
1.	NOT gate
	OR gate
35.	AND gate
44.	None of the above
Aures	. 1
O6.	Digital circuit can be made by the repeated use of
	OR gates
	NOT gates
	NAND sates
	None of the above
Aures	3
(D/7)	The only function of NOT gate is to
10.0	Stop signal
	Invert input signal
3	Act as a universal gate
	None of the above
Aures.	
	When an input signal 1 is applied to a NOT gate, the output is
11.	0
2	1
	Either 0 & 1
4	
Ares	
Acres 1000	

Sample Design Multiple Choice Questions And Answers

Raffaela Di Napoli

Sample Design Multiple Choice Questions And Answers:

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