RealView Assembler User's Guide

Instruction summary

Table 4.1 gives an overview of the instructions available in the ARM, Thumb, and ThumbEE instruction sets. Use it to locate individual instructions and pseudo-instructions described in the rest of this chapter.

Note

Unless stated otherwise, ThumbEE instructions are identical to Thumb instructions.

Table 4.1. Location of instructions

Mnemonic	Brief description	Page	Arch. [1]
ADC, ADD	Add with Carry, Add	*ADD, SUB, RSB, ADC, SBC, and RSC*	All
ADR	Load program or register-relative address (short range)	*ADR*	All
ADRL pseudo-instruction	Load program or register-relative address (medium range)	*ADRL pseudo-instruction*	x6M
AND	Logical AND	*AND, ORR, EOR, BIC, and ORN*	All
ASR	Arithmetic Shift Right	*ASR, LSL, LSR, ROR, and RRX*	All
B	Branch	*B, BL, BX, BLX, and BXJ*	All
BFC, BFI	Bit Field Clear and Insert	*BFC and BFI*	T2
BIC	Bit Clear	*AND, ORR, EOR, BIC, and ORN*	All
BKPT	Breakpoint	*BKPT*	5
BL	Branch with Link	*B, BL, BX, BLX, and BXJ*	All
BLX	Branch with Link, change instruction set	*B, BL, BX, BLX, and BXJ*	T
BX	Branch, change instruction set	*B, BL, BX, BLX, and BXJ*	T
BXJ	Branch, change to Jazelle	*B, BL, BX, BLX, and BXJ*	J, x7M
CBZ, CBNZ	Compare and Branch if {Non}Zero	*CBZ and CBNZ*	T2
CDP	Coprocessor Data Processing operation	*CDP and CDP2*	x6M
CDP2	Coprocessor Data Processing operation	*CDP and CDP2*	5, x6M
CHKA	Check array	*CHKA*	EE

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John R. Mashey

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    ARM Assembly Language William Hohl,Christopher Hinds,2014-10-20 Delivering a solid introduction to assembly language and embedded systems ARM Assembly Language Fundamentals and Techniques Second Edition continues to support the popular ARM7TDMI but also addresses the latest architectures from ARM including CortexTM A Cortex R and Cortex M processors all of which have slightly different instruction sets programmer s models and exception handling Featuring three brand new chapters a new appendix and expanded coverage of the ARM7TM this edition Discusses IEEE 754 floating point arithmetic and explains how to program with the IEEE standard notation Contains step by step directions for the use of KeilTM MDK ARM and Texas Instruments TI Code Composer StudioTM Provides a resource to be used alongside a variety of hardware evaluation modules such as TI s Tiva Launchpad STMicroelectronics iNemo and Discovery and NXP Semiconductors Xplorer boards Written by experienced ARM processor designers ARM Assembly Language Fundamentals and Techniques Second Edition covers the topics essential to writing meaningful assembly programs making it an ideal textbook and professional reference **The Definitive Guide to the ARM Cortex-M0** Joseph Yiu,2011-04-04 The Definitive Guide to the ARM Cortex M0 is a guide for users of ARM Cortex M0 microcontrollers It presents many examples to make it easy for novice embedded software developers to use the full 32 bit ARM Cortex M0 processor It provides an overview of ARM and ARM processors and discusses the benefits of ARM Cortex M0 over 8 bit or 16 bit devices in terms of energy efficiency code density and ease of use as well as their features and applications The book describes the architecture of the Cortex M0 processor and the programmers model as well as Cortex M0 programming and instruction set and how these instructions are used to carry out various operations Furthermore it considers how the memory architecture of the Cortex M0 processor affects software development Nested Vectored Interrupt Controller NVIC and the features it supports including flexible interrupt management nested interrupt support vectored exception entry and interrupt masking and Cortex M0 features that target the embedded operating system It also explains how to develop simple applications on the Cortex M0 how to program the Cortex M0 microcontrollers in assembly and mixed assembly languages and how the low power features of the Cortex M0 processor are used in programming Finally it describes a number of ARM Cortex M0 products such as microcontrollers development boards starter kits and development suites This book will be useful to both new and advanced users of ARM Cortex devices from students and hobbyists to researchers professional embedded software developers electronic enthusiasts and even semiconductor product designers The first and definitive book on the new ARM Cortex M0 architecture targeting the large 8 bit and 16 bit microcontroller market Explains the Cortex M0 architecture and how to program it using practical examples Written by an engineer at ARM who was heavily involved in its development *The Definitive Guide to the ARM Cortex-M3* Joseph Yiu,2011-04-08 This user s guide does far more than simply outline the ARM Cortex M3 CPU features it explains step by step how to program and implement the processor in real world designs It

teaches readers how to utilize the complete and thumb instruction sets in order to obtain the best functionality efficiency and reuseability The author an ARM engineer who helped develop the core provides many examples and diagrams that aid understanding Quick reference appendices make locating specific details a snap Whole chapters are dedicated to Debugging using the new CoreSight technologyMigrating effectively from the ARM7 The Memory Protection Unit Interfaces Exceptions Interrupts and much more The only available guide to programming and using the groundbreaking ARM Cortex M3 processor Easy to understand examples diagrams quick reference appendices full instruction and Thumb 2 instruction sets are all included The author an ARM engineer on the M3 development team teaches end users how to start from the ground up with the M3 and how to migrate from the ARM7 *Arm Assembly Language - An Introduction (Second Edition)* J. R. Gibson,2011 An introductory text describing the ARM assembly language and its use for simple programming tasks

Newark Electronics ,2009 **The Serious Assembler** Charles A. Crayne,1985 Assembler User's Guide J. G. Stasiuk,University of Alberta. Department of Computing Services,1977 *ASSIST* John R. Mashey,1974 **Assembler User Guide** Luigi Pitpitan,2021-07-13 ARM Cortex M3 Assembly Language When a high level language compiler processes source code it generates the assembly language translation of all of the high level code into a processor s specific set of instructions What You ll Learn From This Book Chapter 1 Introduction to Embedded Systems Chapter 2 Microcontrollers and Microprocessors ARM CORTEX Chapter 3 Introduction To Cortex M3 Chapter 4 Introduction To Cortex M4 Chapter 5 Architecture Chapter 6 Cortex M4 Processor Chapter 7 Introduction to Assembly Language Chapter 8 Floating Point Operations Chapter 9 DSP Instruction Set Chapter 10 Controllers Based On Cortex M4 Chapter 11 Project Don t worry if you are new to ARM based controller *Turbo Assembler* Borland,1996 **Borland Turbo Assembler** ,1995 **Turbo Assembler Reference Guide Version 2.0** ,1990 *Extended Assembler* ,1974 **Turbo Assembler** Borland Internacional,1989 **Turbo Assembler** ,1989 **User's Manual, Extended Assembler** Data General Corporation,1974 *Turbo Assembler* ,1993 **Turbo Assembler Version 3.0** ,1991 User's Manual, Absolute Assembler Data General Corporation,1973 Turbo Assembler Version 2.0 ,1990

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